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Application Number		10599205
Filing Date		2006-09-22
First Named Inventor	Yuichiro Sasaki	
Art Unit	TBD	
Examiner Name	TBD	
Attorney Docket Number	41341	

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3	60-138973	JP		1985-07-23			<input checked="" type="checkbox"/>
4	2000-208527	JP		2000-07-28			<input checked="" type="checkbox"/>
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	1	LENOBLE et al, "Reliable and Enhanced Performances of sub-0.1 um pMOSFET's Doped by Low Plasma Doping", VLSI Symposium, IEEE and Japan Society of Applied Physics, pages 110, 2000	<input checked="" type="checkbox"/>
	2	MIZUNO, "Plasma Doping Technique", vol. 70, no. 12, pages 1458-1462, 2001	<input type="checkbox"/>

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CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

☐ That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

OR

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☐ See attached certification statement.

☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/jeffrey j sopko/	Date (YYYY-MM-DD)	2006-11-01
Name/Print	Jeffrey J. Sopko	Registration Number	27676

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